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Gayle W. Chaney  
Date of Signature

*Gayle W. Chaney*  
9/10/04

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Hedler et al.

Group Art Unit: 2815

Serial No.: 10/634,242

Examiner: Unassigned

Filed: August 5, 2003

Docket No.: 1406/158

Confirmation No.: 6396

For: METHOD FOR PRODUCING A SEMICONDUCTOR DEVICE AND  
CORRESPONDING SEMICONDUCTOR DEVICE

\* \* \* \* \*

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.56, 1.97, and 1.98, applicants' undersigned attorney brings to the attention of the Patent and Trademark Office the documents listed on the attached form PTO-1449. Copies of the references as well as Form PTO-1449 are attached hereto. This is not to be construed as a representation that a search has been made or that a reference is relevant merely because cited.

Early passage of the subject application to issue is earnestly solicited.

Appln. No.: 10/634,242

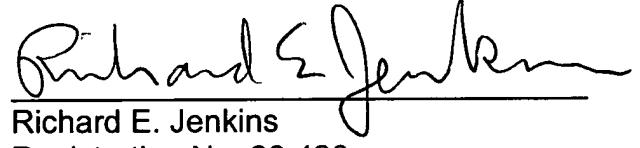
Although it is believed that no fee is due, the Commissioner is hereby authorized to charge any fees associated with the filing of this Information Disclosure Statement to Deposit Account No. **50-0426**.

Respectfully submitted,

JENKINS, WILSON & TAYLOR, P.A.

Date: September 10, 2004

By:

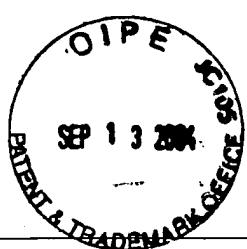
  
Richard E. Jenkins  
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REJ/gwc

Enclosures

Customer No. 25297

1406/158



FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office				Attorney Docket No. 1406/158			Serial No. 10/634,242
List of Documents Cited by Applicant							
				Applicant(s): Hedler et al.			
				Filing Date: August 5, 2003		Group	
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing date if Appropriate
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Name of Patentee or Applicant	Translation Yes   No	
	1.	JP 2001 217337 A	08/10/2001	Japan	Shinko Eelectric Ind. Co. Ltd.	Yes (Abstract Only)	
	2.	JP 2001 319994 A	11/16/2001	Japan	Allied Material Corp.	Yes (Abstract Only)	
	3.	JP 2002 009099 A	01/11/2002	Japan	NEC Kansai Ltd.	Yes (Abstract Only)	
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							

EXAMINER \_\_\_\_\_ DATE CONSIDERED \_\_\_\_\_

\*Examiner Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

*JFW*

JENKINS  
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September 10, 2004



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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: U.S. Patent Application Serial No. 10/634,242 for  
METHOD FOR PRODUCING A SEMICONDUCTOR DEVICE  
AND CORRESPONDING SEMICONDUCTOR DEVICE  
Our Ref. No. 1406/158

Sir:

Please find enclosed in connection with the subject U.S. patent application the following documents:

1. Supplemental Information Disclosure Statement (2 pages);
2. Form PTO-1449 (1 page) in duplicate;
3. Copy of cited references (3 references); and
4. A return-receipt postcard to be returned to us with the U.S. Patent and Trademark Office filing stamp thereon.

The Commissioner is hereby authorized to charge any fees associated with the filing of this correspondence to Deposit Account No. 50-0426.

Respectfully submitted,

JENKINS, WILSON & TAYLOR, P.A.

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